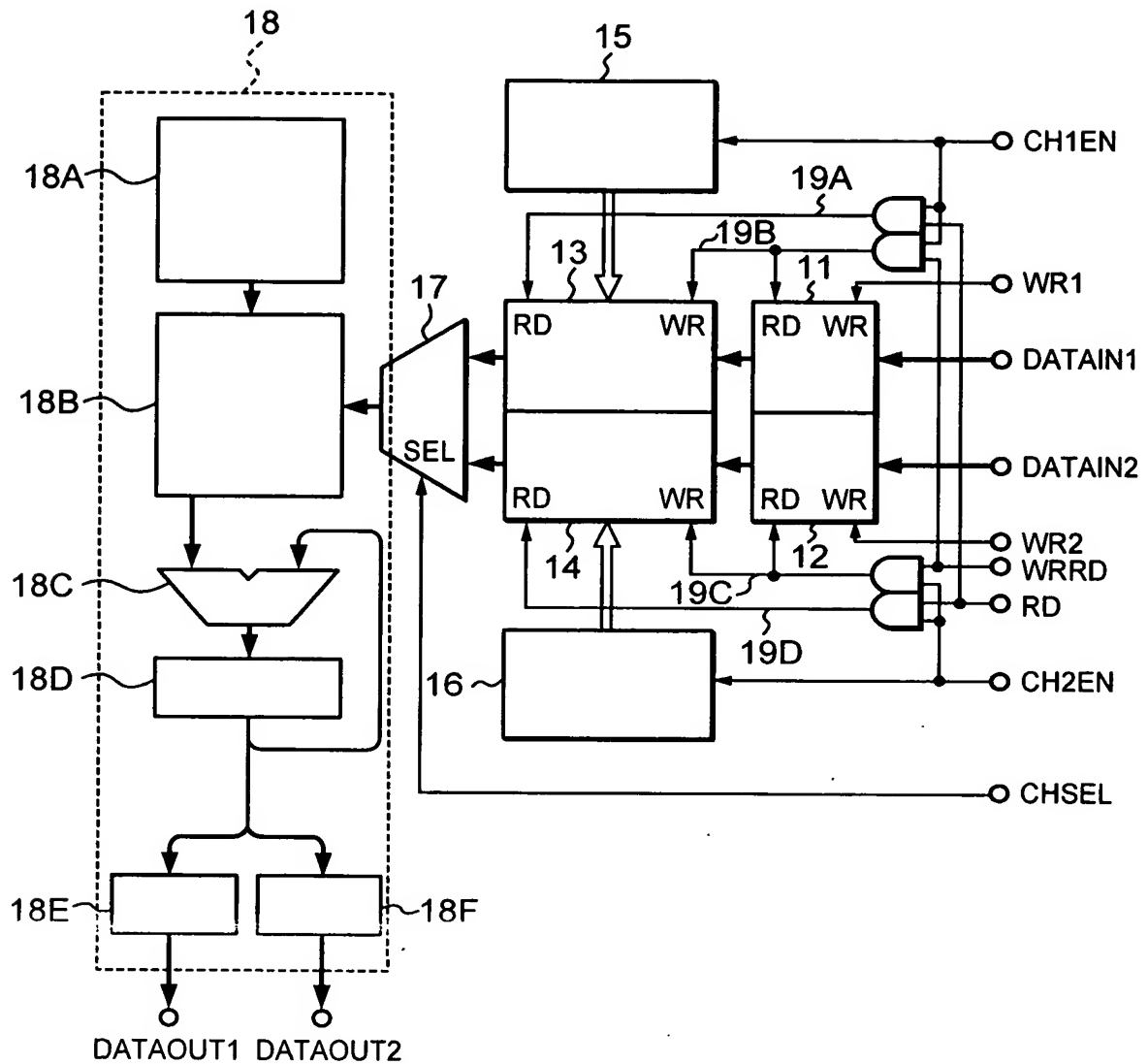
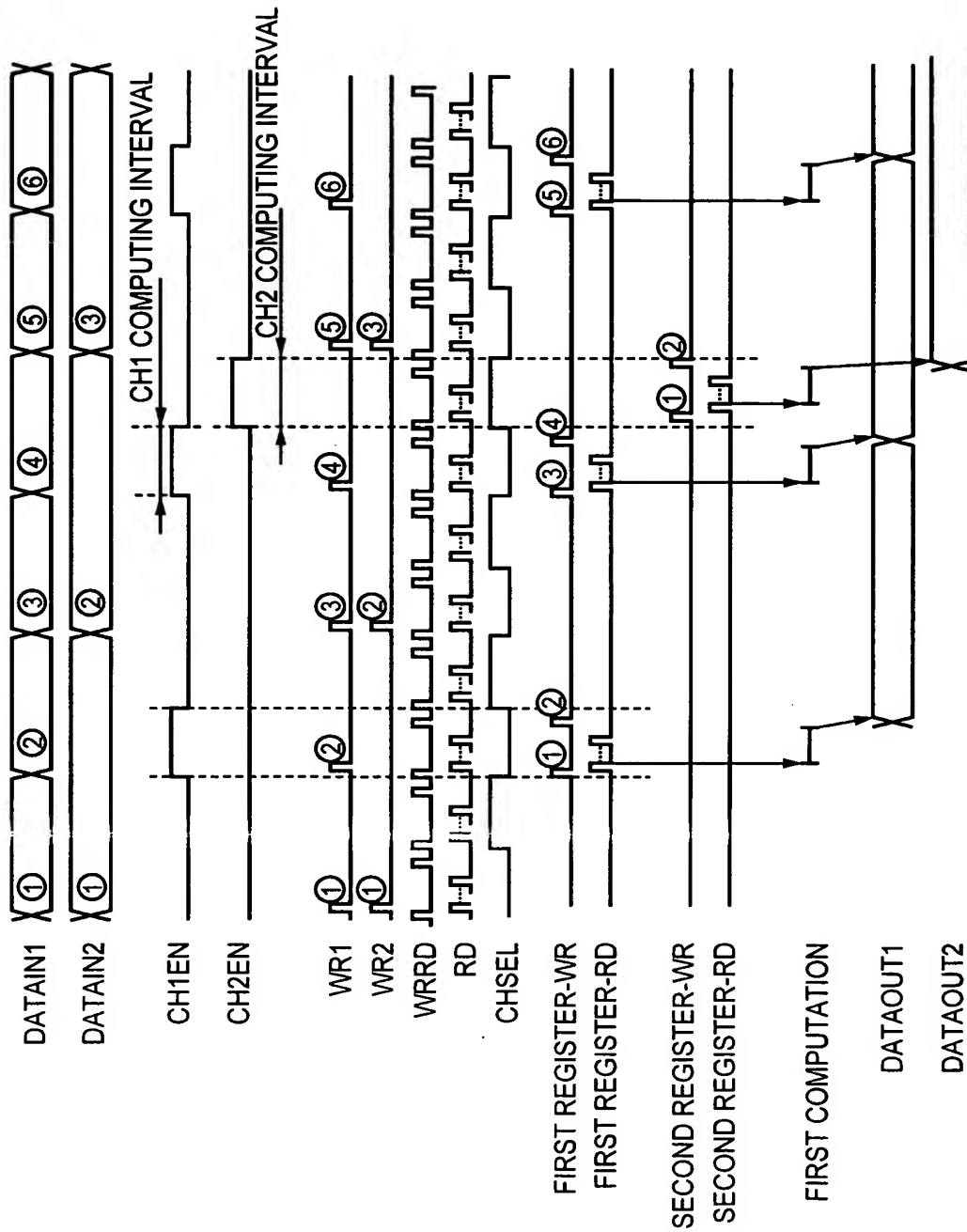


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CIRCUIT CONFIGURATION OF FIRST EMBODIMENT

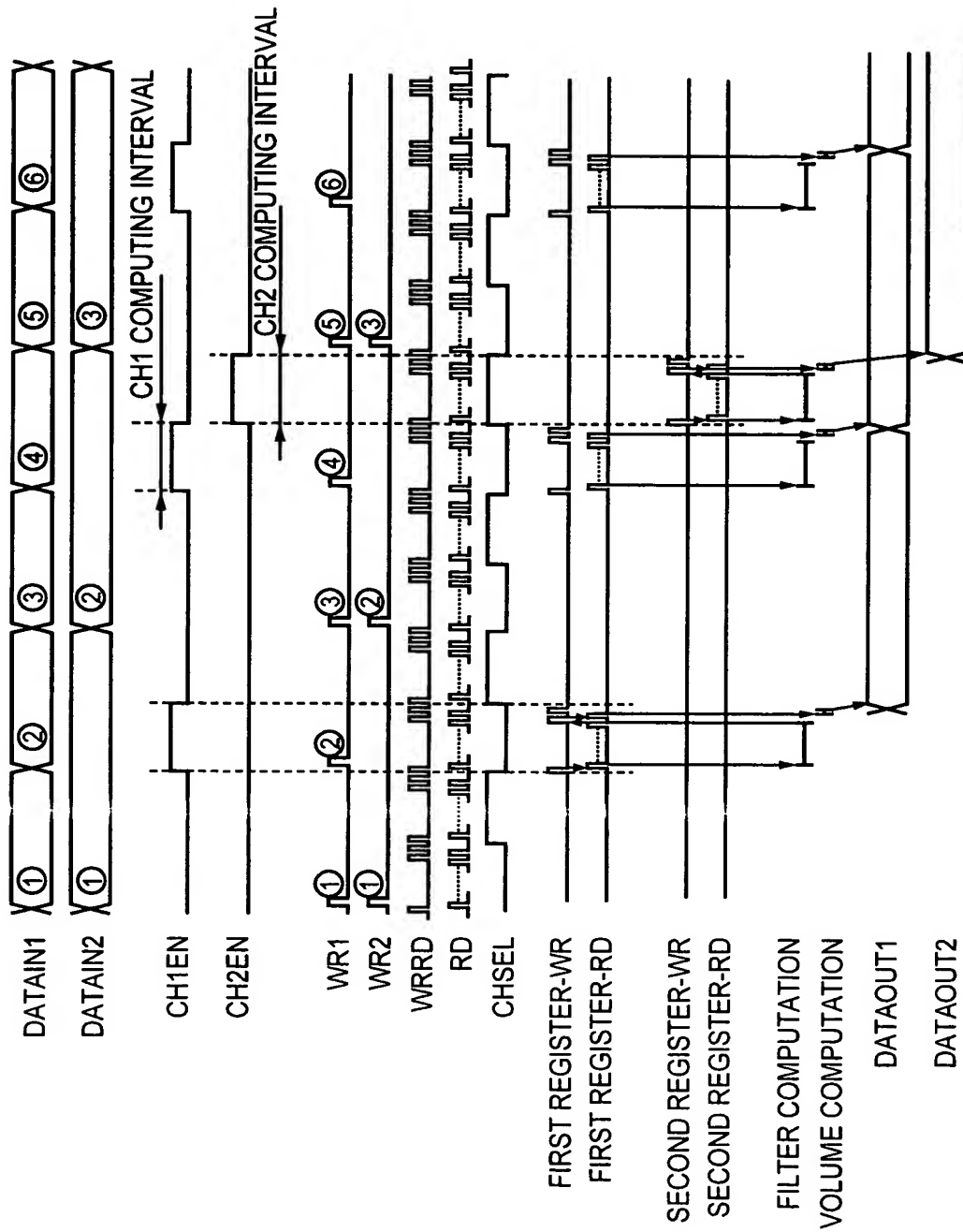
Fig. 1



TIMING CHART OF FIRST EMBODIMENT

Fig. 2

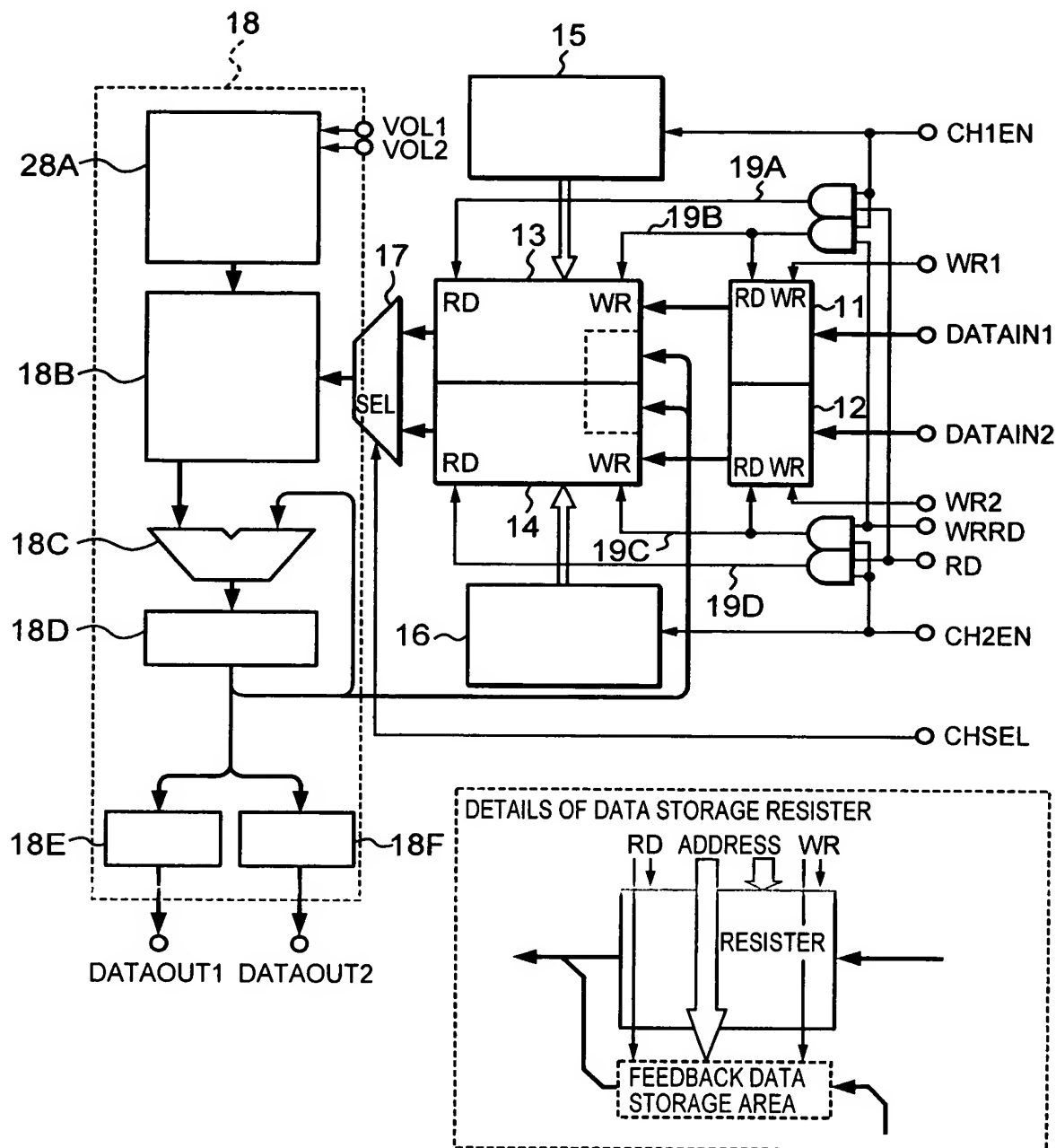
Fig. 3



TIMING CHART OF SECOND EMBODIMENT

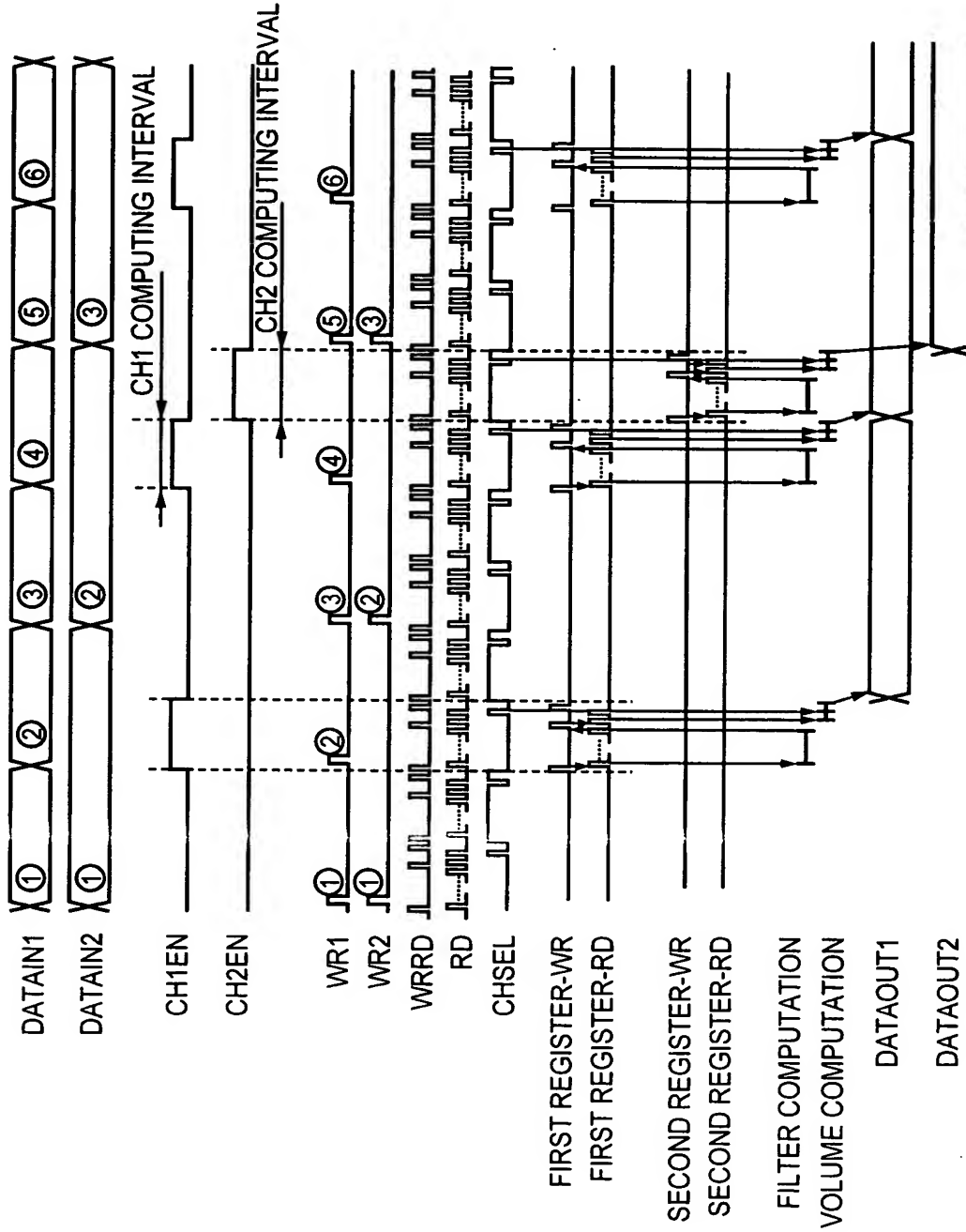
Fig. 4

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CIRCUIT CONFIGURATION OF THIRD EMBODIMENT

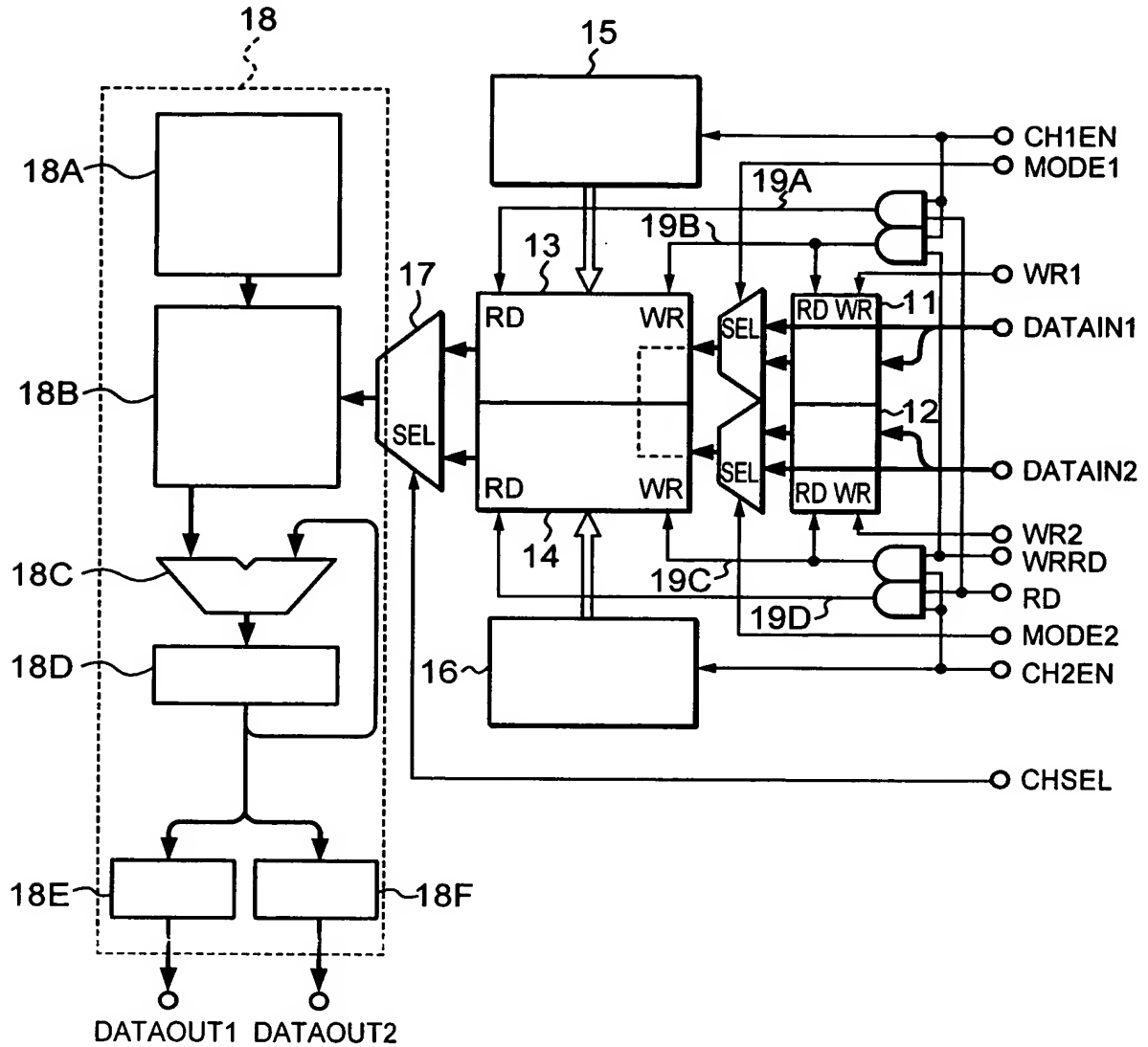
Fig. 5



TIMING CHART OF THIRD EMBODIMENT

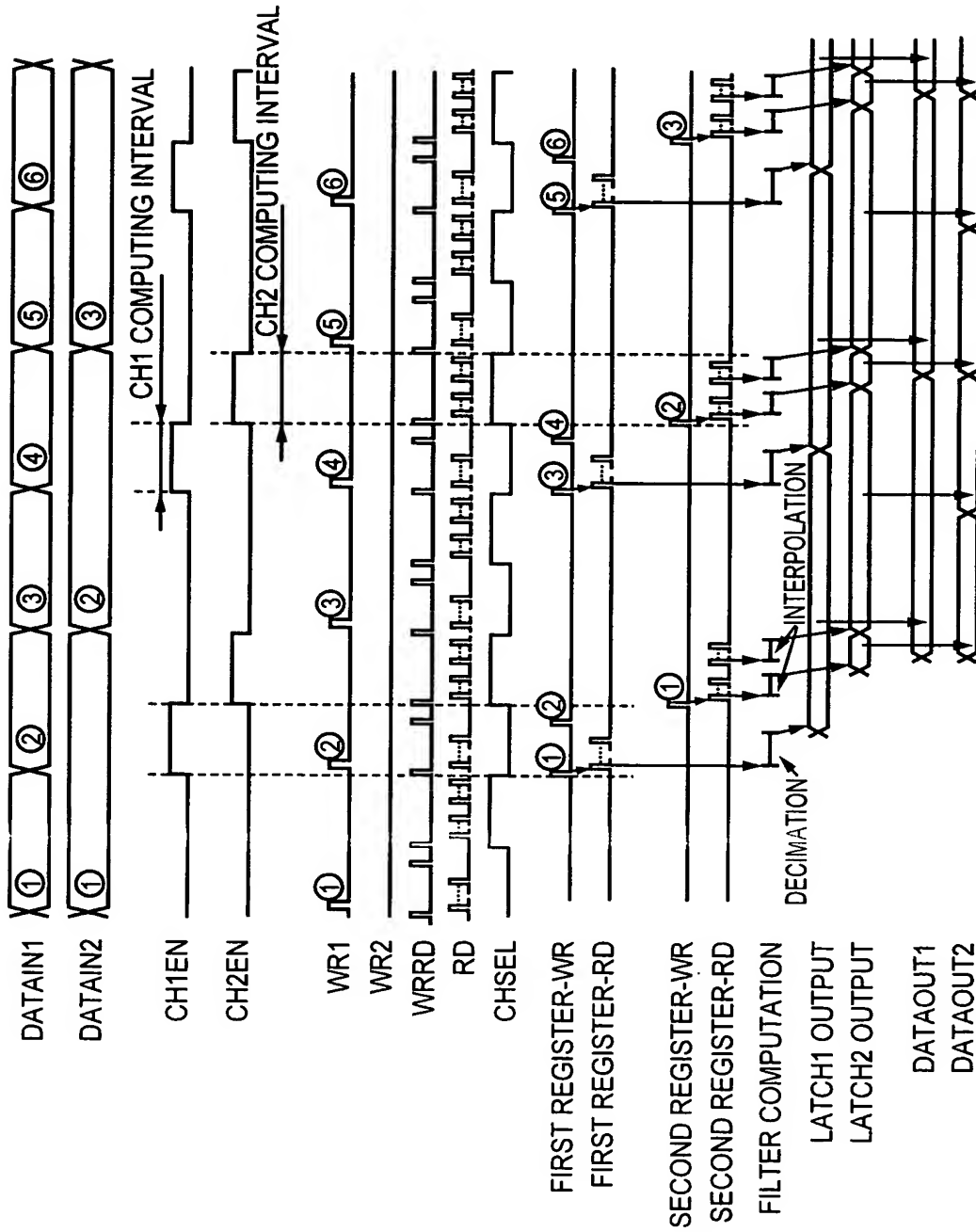
Fig. 6

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CIRCUIT CONFIGURATION OF FOURTH EMBODIMENT

Fig. 7



TIMING CHART OF FOURTH EMBODIMENT

Fig. 8